Freeform Search

Database:
```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Term:
```
priorit$7  near12 thread$3  near9 bit$1
```

Display: `20` Documents in **Display Format:** `TI` **Starting with Number** `1`

Generate: ○ **Hit List** ◉ **Hit Count** ○ **Side by Side** ○ **Image**

[Search]   [Clear]   [Interrupt]

Search History

DATE: Wednesday, June 16, 2004   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
L25	priorit$7 near12 thread$3 near9 bit$1	48	L25
L24	priorit$7 near12 thread$3	1326	L24
L23	L22 not l21	15	L23
L22	lock$3 near10 (flag$1 or bit$1) near20 thread$3 near15 read$5	17	L22
L21	lock$3 near5 (flag$1 or bit$1) near15 thread$3 near15 ready	2	L21
L20	(lock$3 or synchroniz$6 or enabl$6 or activat$4 or wait$4 or inhibit$3 or suspend$3 or suspension or switch$6) near5 bit$1 near15 thread$3 near9 ready	4	L20
L19	L18 not l16	7	L19
L18	(lock$3 or synchroniz$6 or enabl$6 or activat$4 or wait$4 or inhibit$3 or suspend$3 or suspension or switch$6) near5 bit$1 near15 thread$3 near5 (plur$7 or multiple or dual or two or second or child or nested)	55	L18
L17	L16 not l14	11	L17
L16	(lock$3 or synchroniz$6 or enabl$6 or activat$4 or wait$4 or inhibit$3 or suspend$3 or suspension) near5 bit$1 near15 thread$3 near5 (plur$7 or multiple or dual or two or second or child or nested)	48	L16
L15	L14 not l13	8	L15

L14	(lock$3 or synchroniz$6) near5 bit$1 near15 thread$3 near5 (plur$7 or multiple or dual or two or second or child or nested)	37	L14
L13	lock$3 near4 bit$1 near15 thread$3 near5 (plur$7 or multiple or dual or two or second or child or nested)	29	L13
L12	L11 and thread$3	1	L12
L11	bit near1 wise$1 near6 logic$3 near7 programmable	6	L11
L10	bit near1 wise$1 near6 logic$3	427	L10
L9	(programmable or program$4) near9 (mux or multiplex$3 or sel or select$3) near15 bit near3 wise	1	L9
L8	(programmable or program$4) near5 (mux or multiplex$3 or sel or select$3) near15 bit near3 wise	1	L8
L7	(programmable) near5 (mux or multiplex$3 or sel or select$3) near7 bit near3 wise	0	L7
L6	l1 near25 bit near1 wise	4	L6
L5	L1 near15 thread$1	15	L5
L4	L2 and (programmable) near5 (mux or multiplex$3 or sel or select$3) and bit near3 wise	1	L4
L3	L2 and (programmable) near5 (mux or multiplex$3 or sel or select$3) near15 bit near3 wise	0	L3
L2	L1 and thread$1	379	L2
L1	(read or write) near6 register$1 near9 only	7369	L1

END OF SEARCH HISTORY

h   e b   b   g ee e   e b   h   f ff e ch   f